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Total Number of Pages in This Submission

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Perego
Art Unit	2188
Examiner Name	Verbrugge, K.
Attorney Docket Number	RAMB-01049US1

### ENCLOSURES (Check all that apply)

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Signature			
Printed name	Kirk J. DeNiro	1/27/05	
Date	January 24, 2005	Reg. No.	35,854

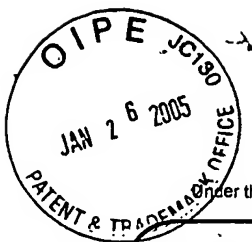
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

### Complete if Known

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### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
		SCI, "P1596: SCI, A Scalable Coherent Interface", November 28, 1988	
		GUSTAVSON et al., DRAFT "The Scalable Coherent Interface Project (SuperBus)", SCI, August 22, 1988, Rev. 14	
		SCHANKE, "Proposal for Clock Distribution in SCI", SCI, May 5, 1989	
		JAMES, "Scalable I/O Architecture for Buses", COMPCON Spring 1989, SCI, November 28, 1988	
		VOLZ et al., "Position Paper On Global Clock For the Futurebus +", SCI, 1989	
		KRISTIANSEN et al., "Scalable Coherent Interface", to appear in Eurobus Conference Proceedings, May 1989	

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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